

Time Table for B. Tech. Course 2016-17							
Session :		2016-17					
Semester :		V Semester					
Name of College :		J I E T C O E					
Branch/ Specialization :		Electronics and Communication Engineering					
Subject/ Lab	Code No.	Time	02 Dec. 2016 Friday	3 Dec. 2016 Saturday	5 Dec. 2016 Monday	6 Dec. 2016 Tuesday	7 Dec. 2016 Wednesday
ELECTRONIC ENGINEERING DESIGN LAB ✓	SEC7A	8.00 am to 11.00 am	D1				
		11.00 am to 2.00 pm	D2				
MICROWAVE ENGINEERING LAB	SEC8A	8.00 am to 11.00 am				D2	D1
		11.00 am to 2.00 pm					
COMMUNICATION LAB-I ✓	SEC9A	8.00 am to 11.00 am			D1		
		11.00 am to 2.00 pm			D2		
SIGNAL PROCESSING LAB-I	SEC10A	8.00 am to 11.00 am					D1
		11.00 am to 2.00 pm				D1	D2 ay
Professional Ethics & Disaster Management ✓	SEC11A	8.00 am to 11.00 am		D1			
		11.00 am to 2.00 pm		D2			

Batch	Roll Numbers
D1	14EJTEC001 TO 14EJTEC017
D2	14EJTEC018 TO 14EJTEC033, 14EJTEC300 & 15EJTEC200

Signature of HOD
29/11/16

Approved
29.11.2016